

App. Serial No. 10/814,426  
Docket No.: 5797-00500

**In the Claims:**

The pending claims are renumbered in accordance with 37 C.F.R. § 1.126 and listed below. No amendments to the claims are made.

1. (Previously Presented) A communication apparatus comprising:  
a radio frequency (RF) circuit for operating on a radio frequency signal; and  
a digital processing circuit coupled to the RF circuit, wherein the digital processing circuit includes:  
a first bus master coupled to a bus;  
one or more other bus masters coupled to the bus; and  
a bus arbiter configured to arbitrate between requests to access the bus by the first bus master and the one or more other bus masters;  
wherein accesses by the one or more other bus masters to the bus are restricted in response to a signal indicative of a change in a mode of operation of the RF circuit.
2. (Original) The communication apparatus as recited in Claim 1 wherein the signal is indicative of a change to an active mode of operation of the RF circuit.
3. (Original) The communication apparatus as recited in Claim 2 wherein the signal indicates a change to a transmission mode of operation of the RF circuit.
4. (Original) The communication apparatus as recited in Claim 2 wherein the signal indicates a change to a reception mode of operation of the RF circuit.
5. (Original) The communication apparatus as recited in Claim 2 wherein the signal is asserted a predetermined amount of time prior to the change to the active mode of operation of the RF circuit.

App. Serial No. 10/814,426  
Docket No.: 5797-00500

6. (Original) The communication apparatus as recited in Claim 1 wherein the signal is asserted a predetermined amount of time prior to a shutdown mode of operation of the digital processing circuit.
7. (Original) The communication apparatus as recited in Claim 1 wherein the signal indicative of a change of mode of operation of the RF circuit is generated by a timing circuit.
8. (Original) The communication apparatus as recited in Claim 1 wherein the first bus master is provided exclusive access to the bus in response to assertion of the signal.
9. (Original) The communication apparatus as recited in Claim 1 wherein the first bus master is a microcontroller unit (MCU).
10. (Original) The communication apparatus as recited in Claim 1 wherein the first bus master is a digital signal processor (DSP).
11. (Original) The communication apparatus as recited in Claim 9 wherein an interrupt signal is provided to the MCU and wherein an interrupt service routine executed by the MCU in response to assertion of the interrupt signal is performed when accesses by masters other than the first bus master to the bus are restricted.
12. (Original) The communication apparatus as recited in Claim 11 wherein the interrupt service routine performs functionality to prepare the digital processing circuit for a shutdown mode of the digital processing circuit.
13. (Original) The communication apparatus as recited in Claim 1 wherein the bus is a multi-layer bus, wherein the first bus master is provided exclusive access to one layer of the bus in response to assertion of the signal while the one or more other bus masters are allowed access to another layer of the multi-layer bus.

App. Serial No. 10/814,426  
Docket No.: 5797-00500

14. (Original) The communication apparatus as recited in Claim 6 wherein the shutdown mode of operation includes disabling at least a portion of the digital processing circuit.
15. (Original) The communication apparatus as recited in Claim 6 wherein the shutdown mode of operation includes disabling a clock that clocks at least a portion of the digital processing circuit.
16. (Original) The communication apparatus as recited in Claim 1 wherein the bus arbiter is configured to restrict the granting of ownership of the bus to the one or more other bus masters in response to the signal.
17. (Original) The communication apparatus as recited in Claim 1 wherein the one or more other bus masters are configured to inhibit requests to gain ownership of the bus in response to the signal.
18. (Original) The communication apparatus as recited in Claim 1 wherein accesses by the one or more other bus masters are restricted by implementing a less favorable arbitration policy for the one or more other bus masters in response to the signal.
19. (Original) The communication apparatus as recited in Claim 1 wherein accesses by the one or more other bus masters to the bus are restricted by terminating burst transfers early in response to the signal.
20. (Original) The communication apparatus as recited in Claim 1 wherein the signal indicative of a change of mode of operation of the RF circuit is generated in response to execution of a software instruction.

App. Serial No. 10/814,426  
Docket No.: 5797-00500

21. (Original) A method of operating a communication apparatus including a radio frequency (RF) circuit and a digital processing circuit, the method comprising:  
    arbitrating between requests to access a bus by a first bus master and one or more other bus masters;  
    receiving a signal indicative of a change in a mode of operation of the RF circuit;  
    and  
    restricting accesses by the one or more other bus masters to the bus in response to the signal.
22. (Original) The method as recited in Claim 21 wherein accesses by the one or more bus masters are restricted by implementing a less favorable arbitration policy for the one or more bus masters in response to the signal.
23. (Original) The method as recited in Claim 21 wherein the signal is indicative of a change to an active mode of operation of the RF circuit.
24. (Original) The method as recited in Claim 23 wherein the signal is asserted a predetermined amount of time prior to the change to the active mode of operation of the RF circuit.
25. (Original) The method as recited in Claim 25 wherein the first bus master is provided exclusive access to the bus in response to assertion of the signal.
26. (Original) A mobile phone comprising:  
    a radio frequency (RF) transceiver for operating on a radio frequency signal; and  
    a digital processing circuit coupled to the RF transceiver, wherein the digital processing circuit includes:  
        a first bus master coupled to a bus;  
        one or more other bus masters coupled to the bus; and  
        a bus arbiter configured to arbitrate between requests to access the bus by the first bus master and the one or more other bus masters;

App. Serial No. 10/814,426  
Docket No.: 5797-00500

wherein accesses by the one or more other bus masters to the bus are restricted in response to a signal indicative of a change in a mode of operation of the RF transceiver.

27. (Original) The mobile phone as recited in Claim 26 wherein the signal is indicative of a change to an active mode of operation of the RF transceiver.
28. (Original) The mobile phone as recited in Claim 27 wherein the signal is asserted a predetermined amount of time prior to the change to the active mode of operation of the RF transceiver.
29. (Original) The mobile phone as recited in Claim 26 wherein the signal is asserted a predetermined amount of time prior to a shutdown mode of operation of the digital processing circuit.
30. (Original) The mobile phone as recited in Claim 26 wherein the signal indicative of a change of mode of operation of the RF transceiver is generated by a timing circuit.
31. (Original) The mobile phone as recited in Claim 26 wherein the first bus master is provided exclusive access to the bus in response to assertion of the signal.
32. (Previously Presented) A mobile phone comprising:  
a radio frequency (RF) front-end circuit for operating on a radio frequency signal;  
a digital processing circuit coupled to the RF front-end circuit, wherein the digital processing circuit includes a first bus master coupled to a bus and one or more other bus masters coupled to the bus; and  
a bus arbiter configured to arbitrate between requests to access the bus by the first bus master and the one or more other bus masters;  
wherein accesses by the one or more other bus masters to the bus are restricted in response to a signal indicative of a change in a mode of operation of the RF front-end circuit; and

App. Serial No. 10/814,426  
Docket No.: 5797-00500

wherein the RF front-end circuit and the digital processing circuit are fabricated on a single integrated circuit chip.

33. (Original) The mobile phone as recited in Claim 32 wherein the signal is indicative of a change to an active mode of operation of the RF front-end circuit.

34. (Original) The mobile phone as recited in Claim 33 wherein the signal is asserted a predetermined amount of time prior to the change to the active mode of operation of the RF front-end circuit.

35. (Original) The mobile phone as recited in Claim 32 wherein the signal is asserted a predetermined amount of time prior to a shutdown mode of operation of the digital processing circuit.

36. (Original) The mobile phone as recited in Claim 32 wherein the signal indicative of a change of mode of operation of the RF front-end circuit is generated by a timing circuit.

37. (Original) The mobile phone as recited in Claim 32 wherein the first bus master is provided exclusive access to the bus in response to assertion of the signal.

38. (Original) A communication apparatus comprising:  
a radio frequency (RF) circuit for operating on a radio frequency signal; and  
a digital processing circuit coupled to the RF circuit, wherein the digital processing circuit includes:  
a first bus master coupled to a bus;  
one or more other bus masters coupled to the bus; and  
a bus arbiter configured to arbitrate between requests to access the bus by the first bus master and the one or more other bus masters;  
wherein accesses by the one or more other bus masters to the bus are restricted in response to a signal asserted a predetermined amount

App. Serial No. 10/814,426  
Docket No.: 5797-00500

of time prior to a shutdown mode of operation of the digital processing circuit.

39. (Original) The communication apparatus as recited in Claim 38 wherein the first bus master is provided exclusive access to the bus in response to assertion of the signal.

40. (Original) The communication apparatus as recited in Claim 38 wherein the shutdown mode of operation includes disabling at least a portion of the digital processing circuit.

41. (Original) The communication apparatus as recited in Claim 38 wherein the shutdown mode of operation includes disabling a clock that clocks at least a portion of the digital processing circuit.

42. (Original) A communication apparatus comprising:  
a radio frequency (RF) circuit for operating on a radio frequency signal; and  
a digital processing circuit coupled to the RF circuit, wherein the digital processing circuit includes:  
a first bus master coupled to a bus;  
one or more other bus masters coupled to the bus; and  
a bus arbiter configured to arbitrate between requests to access the bus by the first bus master and the one or more other bus masters according to an arbitration policy during at least a portion of a duration of an inactive mode of operation of the RF circuit;  
wherein the bus arbiter is further configured to implement a less favorable arbitration policy for the one or more other bus masters in response to a signal indicating a change to an active mode of operation of the RF circuit.

App. Serial No. 10/814,426  
Docket No.: 5797-00500

43. (Original) The communication apparatus as recited in Claim 42 wherein the signal is asserted a predetermined amount of time prior to the change to the active mode of operation of the RF circuit.
44. (Original) The communication apparatus as recited in Claim 42 wherein the first bus master is provided exclusive access to the bus in response to assertion of the signal.
45. (Original) The communication apparatus as recited in Claim 42 wherein the first bus master is a microcontroller unit (MCU).
46. (Original) The communication apparatus as recited in Claim 45 wherein an interrupt service routine executed by the MCU in response to assertion of an interrupt signal is performed when the bus arbiter implements the less favorable arbitration policy for the one or more other bus masters
47. (Original) The communication apparatus as recited in Claim 46 wherein the interrupt service routine performs functionality to prepare the digital processing circuit for a shutdown mode of the digital processing circuit.
48. (Original) The communication apparatus as recited in Claim 42 wherein the RF circuit and the digital processing circuit are integrated on a single chip.
49. (Original) A mobile phone comprising:  
a radio frequency (RF) transceiver for operating on a radio frequency signal; and  
a digital processing circuit coupled to the RF transceiver, wherein the digital processing circuit includes:  
a first bus master coupled to a bus;  
one or more other bus masters coupled to the bus; and  
a bus arbiter configured to allow accesses to the bus by the first bus master and the one or more other bus masters according to an arbitration policy implemented during a first period of operation;



App. Serial No. 10/814,426  
Docket No.: 5797-00500

wherein accesses by the one or more other bus masters to the bus are restricted during a second period of operation in response to a signal asserted a predetermined amount of time prior to a shutdown mode of operation of the digital processing circuit.

50. (Original) The mobile phone as recited in Claim 49 wherein the first bus master is provided exclusive access to the bus during the second period of operation.

51. (Original) The mobile phone as recited in Claim 49 wherein the shutdown mode includes disabling at least a portion of the digital processing circuit.

52. (Original) The mobile phone as recited in Claim 49 wherein the shutdown mode includes disabling a clock that clocks at least a portion of the digital processing circuit.

53. (Original) A communication apparatus comprising:  
a radio frequency (RF) circuit for operating on a radio frequency signal; and  
a digital processing circuit coupled to the RF circuit, wherein the digital processing circuit includes:  
a first bus master coupled to a bus;  
one or more other bus masters coupled to the bus; and  
a bus arbiter configured to allow accesses to the bus by the first bus master and the one or more other bus masters according to an arbitration policy implemented during a first period of operation;  
wherein accesses by the one or more other bus masters to the bus are restricted during a second period of operation beginning a predetermined amount of time prior to an active mode of the RF circuit.

54. (Original) The communication apparatus as recited in Claim 53 wherein the first bus master is provided exclusive access to the bus during the second period of operation.

App. Serial No. 10/814,426  
Docket No.: 5797-00500

55. (Original) The communication apparatus as recited in Claim 53 wherein the second period of operation is controlled by a timing circuit.

56. (Original) The communication apparatus as recited in Claim 53 wherein the bus arbiter is configured to implement a less favorable arbitration policy for the one or more other bus masters during the second period of operation.